

AALAP TRIPATHY

4302 COLLEGE MAIN ST #510 • BRYAN, TX 77801

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SUMMARY OF QUALIFICATIONS

- Algorithm Development Environments: LabVIEW, MATLAB
- Hardware Description Languages: VHDL, Verilog, PSoC Designer 5.0
- High Level Languages: C/C++, JAVA
- Design Automation Tools: PSpice A/D, ModelSim, ISE Foundation XILINX, TI Code Composer Studio, Freescale CodeWarrior Dev. Studio
- Other Design Tools: PHP, ASP.net, Visual Basic, mySQL, MS Access, Oracle 9i
- Scripting languages: csh, ksh, Perl, sed, sh, Tcl/Tk

EDUCATION

August 2008 – Present

Texas A&M University
College Station, TX

Computer Engineering, Ph.D.

- Expected graduation: May 2012
- Advisor: Dr Rabi Mahapatra, Embedded Systems Co-design Group GPR: 3.25/4.0
- Research Interests: System on Chip Design, Semantic Routed Networks
- Publication: “Representation of Complex Concepts for Semantic Routed Networks”, ICDCN 2009

August 2004 – June 2008

Birla Inst of Tech & Science - Pilani
Goa Campus, INDIA

Electrical & Electronics Engineering, B.E.

- CGPA: 9.45/10
- 3 time recipient of BITS Merit Scholarship awarded to the top 1% students
- President, Electrical & Electronics Engineering Student’s Association
- Publications: “Server Implementations for Campus Wide Networks – DNS, Proxy, LDAP, VOIP”, “Video Conferencing over low bandwidth networks” with Microsoft Research for Conference® XP, “Neural Networks in Production Functions for Steady State Macroeconomic Modeling”

WORK EXPERIENCE

Teaching Assistant

Texas A&M University
College Station, TX

September 2008 – Present

- Assist and evaluate students in CPSC 483 – Computer System Design

Co-op Bachelor, PPSoc BU, DCD

Cypress Semiconductor Corp.
San Jose, CA

January 2008 – June 2008

- Prototype Development, Competitive Analysis, Pricing for Power line Comm. Solution NPP
- Presented Power line Communication Solution at Cypress’s FAE Conference, April 2008
- Authored Advance Datasheet and User’s Guide for Powerline Communications Technology
- First Author on Cypress University (CYU) Courses Power Electronics – I and Power Electronics-II

Teaching Assistant

Birla Inst of Tech & Science- Pilani
Goa Campus, INDIA

January 2006 – December 2006

- Conducted Lab Sessions for a class of 60 students for Introductory Programming
- Set question papers & evaluated responses during bi-weekly online examinations

Engineering Trainee

Bhabha Atomic Research Center
Mumbai, INDIA

May 2005 – August 2006

- “Incremental Backup Deployment for the ANUPAM-AMEYA Supercomputer”
- Package Recommended for major supercomputing establishments in India

EXTRACURRICULAR ACTIVITIES

- Managing BITS Alumni Association International’s Tech Team, <http://www.bitsaa.org>
- Designing 360° Virtual Panoramas – BITS 360 Development Team, <http://www.bits360.com>